FORM PTO-1449 (MODIFIED)

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LIST OF PATENTS AND PUBLICATIONS
FOR APPLICANTS INFORMATION
DISCLOSURE STATEMENT

(USE SEVERAL SHEETS IF NECESSARY)

FILING DATE: July 23, 2001

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APPLICANTS:
Harley Kent HEINRICH and Vijay PILLAI

Filing Date

Reference Designation

U.S. Patents

Examiner						lass if
Initial	Document Number	Date	Name	Class	_appro	priate
KL	AA 4,724,427	02/09/88	Carroll		echnology	
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KL	AD 6,046,676	04/04/00	Ward et al.			, C
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	AQ OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)					
	AR By D. Friedman, H. Heinrich and ISSCC97/Session 17/TD: Low Power/Low-Voltage					
			per SA 17.5; February 8, 1997/ S			
	D.W. Djar; "A Low-Power CMOS Integrated Circuit For Field-Powered		rnational Solid State Circuits Con			
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EVANDE			DATE CONCIDERED	0/1/211	, 	
EXAMINE	R KIET LE		DATE CONSIDERED (2/11/04		

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